



Welcome United States Patent and Trademark Office

☐ Search Results

## BROWSE

## SEARCH

## IEEE XPLORE GUIDE

Results for "(graziano m.&lt;in&gt;au)"

Your search matched 14 of 1166705 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» [View Session History](#)» [New Search](#)

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

## Modify Search

(graziano m.&lt;in&gt;au)

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

## Select Article Information

- ☐ 1. **An electromigration and thermal model of power wires for a priori high-level reliability analysis**  
Casu, M.R.; Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on  
Volume 12, Issue 4, April 2004 Page(s):349 - 358  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(648 KB\)](#) IEEE JNL
- ☐ 2. **The ChARM tool for tuning embedded systems**  
Prete, C.A.; Graziano, M.; Lazzarini, F.;  
Micro, IEEE  
Volume 17, Issue 4, July-Aug. 1997 Page(s):67 - 76  
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(416 KB\)](#) IEEE JNL
- ☐ 3. **The key to consistency**  
Graziano, M.;  
Manufacturing Engineer  
Volume 78, Issue 5, October 1999 Page(s):209 - 211  
[AbstractPlus](#) | Full Text: [PDF\(196 KB\)](#) IEE JNL
- ☐ 4. **Noise-tolerance analysis for high speed CMOS circuits**  
Graziano, M.; Masera, G.; Piccinini, G.; Roch, M.R.; Zamboni, M.;  
Microelectronics, 1998. ICM '98. Proceedings of the Tenth International Conference on  
14-16 Dec. 1998 Page(s):37 - 40  
[AbstractPlus](#) | Full Text: [PDF\(396 KB\)](#) IEEE CNF
- ☐ 5. **A global optimization tool for CMOS logic circuits**  
Delaurenti, M.; Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Electronics, Circuits and Systems, 1999. Proceedings of ICECS '99. The 6th IEEE International Conference on  
Volume 3, 5-8 Sept. 1999 Page(s):1671 - 1674 vol.3  
[AbstractPlus](#) | Full Text: [PDF\(312 KB\)](#) IEEE CNF
- ☐ 6. **A statistical noise-tolerance analysis and test structure for logic families**  
Graziano, M.; Masera, G.; Piccinini, G.; Ruo Roch, M.; Zamboni, M.;  
Microelectronic Test Structures, 1999. ICMTS 1999. Proceedings of the 1999 International Conference on  
15-18 March 1999 Page(s):68 - 73  
[AbstractPlus](#) | Full Text: [PDF\(748 KB\)](#) IEEE CNF

7. **Cell library development using multi-objective function optimization**  
Delaurenti, M.; Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
ASIC/SOC Conference, 2000. Proceedings. 13th Annual IEEE International  
13-16 Sept. 2000 Page(s):402 - 406  
[AbstractPlus](#) | Full Text: [PDF\(360 KB\)](#) IEEE CNF
8. **Transistors optimization of CMOS logic structures for high performance IC**  
Delaurenti, M.; Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Microelectronics, 1999. ICM '99. The Eleventh International Conference on  
22-24 Nov. 1999 Page(s):157 - 160  
[AbstractPlus](#) | Full Text: [PDF\(288 KB\)](#) IEEE CNF
9. **A noise test structure for CMOS logic families**  
Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Microelectronics, 1999. ICM '99. The Eleventh International Conference on  
22-24 Nov. 1999 Page(s):93 - 96  
[AbstractPlus](#) | Full Text: [PDF\(456 KB\)](#) IEEE CNF
10. **Noise safety design methodologies**  
Graziano, M.; Delaurenti, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Quality Electronic Design, 2000. ISQED 2000. Proceedings. IEEE 2000 First Internatio  
on  
20-22 March 2000 Page(s):157 - 164  
[AbstractPlus](#) | Full Text: [PDF\(240 KB\)](#) IEEE CNF
11. **Automated power supply noise reduction via optimized distributed capacitors in**  
Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Mixed-Signal Design, 2001. SSMSD. 2001 Southwest Symposium on  
25-27 Feb. 2001 Page(s):167 - 172  
[AbstractPlus](#) | Full Text: [PDF\(416 KB\)](#) IEEE CNF
12. **Switching noise analysis framework for high speed logic families**  
Delaurenti, M.; Graziano, M.; Masera, G.; Piccini, G.; Zamboni, M.;  
VLSI Design, 2001. Fourteenth International Conference on  
3-7 Jan. 2001 Page(s):524 - 530  
[AbstractPlus](#) | Full Text: [PDF\(692 KB\)](#) IEEE CNF
13. **Power supply wires self-heating analysis**  
Casu, M.; Graziano, M.; Roch, M.R.; Viglione, F.;  
Microelectronics, 2001. ICM 2001 Proceedings. The 13th International Conference on  
29-31 Oct. 2001 Page(s):115 - 118  
[AbstractPlus](#) | Full Text: [PDF\(472 KB\)](#) IEEE CNF
14. **Power bus optimal sizing in presence of power supply noise**  
Graziano, M.; Masera, G.; Piccinini, G.; Zamboni, M.;  
Microelectronics, 2001. ICM 2001 Proceedings. The 13th International Conference on  
29-31 Oct. 2001 Page(s):111 - 114  
[AbstractPlus](#) | Full Text: [PDF\(522 KB\)](#) IEEE CNF




[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) |

Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((logarithm &lt;and&gt; fractional)&lt;in&gt;metadata)"

Your search matched 6 of 1166705 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

e-mail

» [View Session History](#)» [New Search](#)

Modify Search

» Key

&gt;&gt;

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract

Select Article Information

- ☐ 1. **Layered optical property assessment by reflectance**  
 Cui, W.; Ostrander, L.E.;  
 Bioengineering Conference, 1990., Proceedings of the 1990 Sixteenth Annual Northea  
 26-27 March 1990 Page(s):105 - 106  
[AbstractPlus](#) | Full Text: [PDF\(168 KB\)](#) IEEE CNF
- ☐ 2. **On the asymptotic capacity of stationary Gaussian fading channels**  
 Lapidoth, A.;  
 Information Theory, IEEE Transactions on  
 Volume 51, Issue 2, Feb. 2005 Page(s):437 - 446  
[AbstractPlus](#) | Full Text: [PDF\(272 KB\)](#) IEEE JNL
- ☐ 3. **In-service monitoring for cell loss quality of service violations in ATM networks**  
 Hongbo Zhu; Frost, V.S.;  
 Networking, IEEE/ACM Transactions on  
 Volume 4, Issue 2, April 1996 Page(s):240 - 248  
[AbstractPlus](#) | Full Text: [PDF\(872 KB\)](#) IEEE JNL
- ☐ 4. **Effects of heat and pressure on surface light reflectance at 660 and 880 nm**  
 Abrahams, J.; Bhattacharyya, S.K.; Ostrander, L.E.; Cui, W.;  
 Bioengineering Conference, 1991., Proceedings of the 1991 IEEE Seventeenth Annua  
 4-5 April 1991 Page(s):137 - 138  
[AbstractPlus](#) | Full Text: [PDF\(136 KB\)](#) IEEE CNF
- ☐ 5. **Discrimination of natural homogeneous textures based on fractal models**  
 Limas Serafim, A.F.;  
 Industrial Electronics, 1997. ISIE '97., Proceedings of the IEEE International Symposiu  
 7-11 July 1997 Page(s):773 - 778 vol.3  
[AbstractPlus](#) | Full Text: [PDF\(668 KB\)](#) IEEE CNF
- ☐ 6. **Efficient conversion algorithms for long-word-length binary logarithmic numbers implementation**  
 Wan, Y.; Khalil, M.A.; Wey, C.-L.;  
 Computers and Digital Techniques, IEE Proceedings-  
 Volume 146, Issue 6, Nov. 1999 Page(s):295 - 301  
[AbstractPlus](#) | Full Text: [PDF\(548 KB\)](#) IEE JNL



Indexed by  
 Inspec

[Help](#) [Contact Us](#) [Privacy &](#)

© Copyright 2005 IEEE –